## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

S. IKEDA et al.

Serial No.:

To be assigned

Filed:

April 8, 2004

For:

SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD

FOR MANUFACTURING THE SAME

Group:

n/a

Examiner:

n/a

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.97 & 1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 April 8, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO-1449 which lists documents cited in the related applications including application Serial No. 10,379,543 filed March 6, 2003, application Serial 09/372,007, filed August 11, 1999, now U.S. Patent No. 6,436,753 issued August 20, 2002, and application Serial No.

10/157,978, filed May 31, 2002, now U.S. Patent No. 6,559,006 issued May 6, 2003.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Ronald J. Shore

Registration No. 28,577

RJS/kmh (703) 312-6600

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.37425CC3	SERIAL NO. To be assigned			
INFORMAT	ION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT S. IKEDA et al				
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## **U.S. PATENT DOCUMENTS**

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							Yes	No
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